

- [1] R. Dehghani . S. M. Atarodi “A Low Power Wideband 2.6GHz CMOS Injection-Locked Ring Oscillator Prescaler/ “2002 Radio Frequency Integrated Circuits Symposium Philadelphia /2002 (RFIC 2002)
- [2] R. Dehghani, S. M. Atarodi “A 2.5 GHZ LOW POWER FULLY INTEGRATED – CONTROLLED FRACTIONAL –N FREQUENCY SYNTHESIZER/ “2003 IEEE International Frequency Control Symposium/Florida/2003 (FCS 2003)
- [3] Mehran Nekuii, S. M. Atarodi “A FAST CONVERGING ALGORITHM FOR NETWORK ECHO CANCELLATION/”IEEE Journal of signal Processing Letters/2003
- [4] Amin Safarian, Farzad Sahandi, S. M. Atarodi “A New Low-Power Sigma-Delta Modulator With The Reduced Number Of Op-Amp For Speech Band Applications/”IEEE International Symposium on Circuit and Systems/Bangkok/2003. (ISCAS 2003)
- [5] Armin Tajalli, and S. M. Atarodi “STRUCTURED DESIGN OF AN INTEGRATED SUBSCRIBER LINE INTERFACE SYSTEM AND CIRCUIT/ “IEEE International Symposium on Circuits and Systems/Bangkok/2003. (ISCAS 2003)
- [6] Armin Tajalli, and S. M. Atarodi “ A COMPACT BIQUADRATIC GM-C FILTER STRUCTURE FOR LOW-VOLTAGE AND HIGH FREQUENCY APPLICATIONS/ “ IEEE International Symposium on Circuit and Systems/Bangkok/2003. (ISCAS 2003)
- [7] Armin Tajalli, and S. M. Atarodi “ DESIGN CONSIDERATIONS FOR 1 .5-7, 10.7-MHZ BANDPASS GM-C FILTER IN A 0.6-UM STANDARD CMOS TECHNOLOGY/ “ IEEE International Symposium on Circuit and Systems/ Bangkok /2003. (ISCAS 2003)
- [8] S. Mehrmanesh, M. B. Vahidfar, H. A. Aslanzadeh, S. M. Atarodi “A 1-VOLT, HIGH PSRR, CMOS BANDGAP VOLTAGE REFERENCE/ “IEEE International Symposium on Circuit and Systems/Bakgkok /2003 .(ISCAS 2003)
- [9] S. Mehrmanesh, H. A. Aslanzadeh, M. B. Vahidfar, S. M. Atarodi “A 1.5 high-speed class AB operational amplifier for high-resolution high-speed pipelined A/D / “IEEE International Symposium on Circuit and Systems/Bangkok/2003. (ISCAS2003)
- [10] S. Mehrmanesh, M. B. Vahidfar, H. A. Aslanzadeh, S. M. Atarodi “ An Ultra Low-Voltage

Gm-C Filter For Video Applications / “IEEE International Symposium on Circuit and Systems/Bangkok \2003 . (ISCAS 2003)

[11] H. A. Aslanzadeh, S. Mehrmanesh, M. B. Vahidfar, S. M. Atarodi “1.8-V HIGH-SPEED 12-BIT PIPELINED ANALOG TO DIGITAL CONVERTER FOR DI.../ “IEEE International Symposium on Circuit and Systems/Bangkok /2002 .(ISCAS 2002)

[12] M. B. Vahifar, A. Tajalli, S. Mehrmanesh, S. M. Atarodi “ A 90-V Subscriber Line Interface Circuit (SLIC) in A CMOS Technology/ The 14th International Conference on Microelectronics/Beirut/2002 (ICM 2002)

[13] M. Nekuii, and S. M. Atarodi “COMPOSITE PNLMS & NLMS ADAPTATION : A new method for network echo cancellation/2002 14th International Conference on Digital Signal Processing /Thera-Greece/2002 (DSP 2002)4

[14] S. Mehrmanesh, S. M. Atarodi “ A High dynamic range CMOS variable gain filter for ADSL/ “ IEEE International Symposium on Circuit and Systems/ Arizona /2002. (ISCAS 2002)

[15] A. Tajalli, S. M. Atarodi “A 1.8V supply, 22-MHz cut-off frequency, g/sub m/-C filter in a 0.2um standard CMOS technology / “ The 14th International Conference on Microelectronics 2002/ Beirut/2002 (ICM, 2002)

[16] A. Tajalli, S. M. Atarodi “A 1.5-V supply, 10.7-MHz , bandpass gm-C filter in a 0.6um standard CMOS technology / “ The 14thInternational Conference on Microelectronics/Beirut/2002 (ICM 2002)

[17] S. Mehrmanesh, H. A. Aslanzadeh, M. B. Vahidfar, S. M. Atarodi “ A 1.8V HIGH DYNAMIC RANCE COMS GM-C FILTER FOR PORTABLE VIDEO SYSTEMS/”The 14th International Conference on Microelectronics/Beirut/2002 (ICM 2002)

[18] V. M. Tousi, F.Sahandi, S. M. Atarodi Shojaei “ A 3.3 V/1 W class D audio power amplifier with 103 dB DR and 90% efficiency/”22rd International Conference on Microelectronics /Nis/2002/(MIEL 2002)

[19] Aida Varzaghani, and S. M. Atarodi “ A NEW METHOD TO INCREASE THE DYNAMIC RANGE OF SWITCHED OPAMP DELTA-SIGMA MODULATORS/ “IEEE

International Symposium on Circuits and Systems/Arizona/2002 (ISCAS 2002)

[20] M. Vahidfar, Armin Tajalli, S. M. Atarodi "A Low-Power Subscriber Line Interface Circuit in High-Voltage CMOS Technology/ "IEEE International Symposium on Circuit and Systems/Arizona/2002 (ISCAS 2002)

[21] A. Tajalli, S. M. Atarodi A. Abidi " A 1.5V Supply, Video Ranger Frequency, Gm-C Filter /" The 2000 IEEE International Symposium on Circuits and Systems/Geneva/2000 (ISCAS 2000)

[22] P.H. Shanjani and S. M. Atarodi "A High Dynamic –Range, High Linear, Self-Tuned GmC Filter For Video Range Application / "IEEE International Symposium on Circuits and Systems/Florida /1999 (ISCAS 99).

[23] S. M. Atarodi j.Jr.Choma, "High-frequency fully integrated OTA-C filters using a 7.2 GHz bipolar OTA/ "36th Midwest Symposium on Circuits and Systes/Mexico/1993.

[24] Q. A. Safarian, F. sahandi, S. M. Atarodi "A new single-loop single-stage low power sigma-delta modulator / "The 14th International Conference on Microelectronics/Beirut/2002 (ICM 2002)

[25] R. Lotfi, S. M. Atarodi "A low-voltage low-power switched-capacitor filter using a new transistor configuration/ " The 12th International Conference on Microelectronics/Tehran/2000 (ICM 2000)5

[26] P. H. Shanjani S. M. Atarodi " A Novel Q Tuning Method, Using Variable Output Conductance of Transconductors / " The Tenth International Conference on Microelectronic /Monastir/1998 (ICM 1998)

[27] P. H. Shanjani, S. M. Atarodi " A high dynamic –range, highly linear, video –range filter / "the Tenth International Conference on Microelectronics/Monastir/1998(ICM 1998)

[28] R. Dehghani, S. M. Atarodi B. Bornoosh A. Afzali Kusha " A Reduced Complexity Low Voltage 1-Bit High-Order Digital Delta-Sigma Modulator for Fractional –N Frequency Synthesis / Midwest Symposium on Circuits & Systems/Oklahoma/2002.

[29]

رضا لطفی ، سیدمجتبی عطاردی "طراحی و بهینه سازی یک ترکیب ترانزیستوری جدید برای استفاه در مدارهای مجتمع
1379 (ICEE 2000)/ آنالوگ ولتاژ پائین / " هشتمین کنفرانس مهندسی برق ایران /اصفهان

[30]

سیدآرمین تجلی ، سیدمجتبی عطاردی ، اکبر ادیبی " فیلتر ترارسانا – خازن مجتمع با تغذیه 2/1 ولت برای فرکانسهای
1279 (ICEE 2000) / ویدیویی / " هشتمین کنفرانس مهندسی برق ایران / اصفهان

[31]

سیدحامد ساداتی و سید مجتبی عطاردی " طراحی سنتز کننده فرکانس ولتاژ پائین به صورت مجتمع با بهبود در بخش
1379 (ICEE 2000) / کنترل / " هشتمین کنفرانس مهندسی برق ایران / اصفهان

[32] Pooya Torkzadeh, Armin Tajalli , and Mojtaba Atarodi " A Wide Tuning Range, 1 GHz-
2.5GHz DLL-Based Fractional Frequency Synthesizer / " IEEE (ISCAS 2005) to be presented
on May 2002

[33] P. Torkzadeh, A. Tajalli, and S. M. Atarodi " A LOW JITTER, 1-2.5 GHZ DLL-BASED
FRACTIONAL FREQUENCY MULTIPLIER FOR ON CHIP CLOCK GENERATION
APPLICATIONS/ " IEEE (ISCAS 2005) to be presented on May 2005

[34] Armin Tajalli, Pooya Torkzadeh, and S. M. Atarodi " A Fractional Multiplying Delay Locked
Loop For On Chip Clock Generation Applications / " IEEE (ISCAS 2005)

[35] Majid Shalchian, S. M. Atarodi " A Logic CMOS Compatible Flash EEPROM for Small
Scale Integration " / IEEE International Conference on Microelectronics, 2005

[36] Rasoul Dehghani and S. M. Atarodi " A New Low Voltage Precision CMOS Current
Reference With No External Components " IEEE Transactions on Circuits & Systems, II Vol.
50, No.12 pp 928-932

[37] Armin Tajalli, S. M. Atarodi " Linear phase detection using two-phase latch " / Electronics
Letters, 27th Nov. 2003, Vol.29, No.24, pp 1695-1696

[38] M. Shalchian, J. Grisolia, G. Ben Assayag and H. Coffin, S. M. Atarodi " Roomtemperature
quantum effect in silicon nanoparticles obtained by low-energy implantation and
embedded in a nanometer scale capacitor / Applied Physics Letters, 86.163111, 2005

[39] M. Shalchian, J. Grisolia, G. Ben Assayag, H. Conffin, S. M. Atarodi , A. Claverie " From
Continuous to quantized charging response of silicon nanocrystals obtained by ultra-low energy
ion implantation / " Accepted For Publication at Solid State Electronics " " "

[40] A. Tajalli, and S. M. Atarodi " A Duty-Cycle Controller for Low-Jitter Frequency Doubling
DLL, " To be Published in IEE Circuits, Devices & Systems Journal (August 2005)

- [41] A. Tajalli, and S. M. Atarodi “ A Wide Tuning Range, Fractional Multiplying Delay Locked Loop Topology for Frequency Hopping Applications “ Accepted For Publication on Analog Integrated Circuits & Signal processing Journal. To be published in 2005.
- [42] A. Tajalli, and S. M. Atarodi “ Low-Voltage CMOS Transconductor-C-Filter Design Using Charge-Pump Circuit “Accepted For Publication on Analog Integrated Circuit & Signal processing Journal. To be published in 2005.
- [43] A. Tajalli, and S. M. Atarodi “ Structured Design of Integrated Subscriber Line Interface System and Circuit “ Accepted For Publication on Analog Integrated Circuit & Signal processing Journal. To be published in 2005.
- [44] مجیدشالچیان، سیدمجتبی عطاردی "مجمع سازی حافظه فلاش با استفاده از یک لایه پلی-سیلیکان در پروسه متداول CMOS میکرومتری 25/0 در ابعاد /2004، No.2، Vol.1، Iranian Association of Electrical and Electronics Engineers/
- [45] S. M. Atarodi and J. Choma, Jr. “A 7.2 GHz Bipolar Operational Transconductance Amplifier for fully Integrated OTA-C Filter ,” Analog Integrated Circuits and Signal Processing, 2, pp 243-252, 1994.
- [46] S. M. Atarodi and Armin Tajalli.”A Low-Power, Multichannel Gated Oscillator-Based CDR for Short-Haul Applications” ISLPED (131) San Diego, California
- [47] Armin Tajalli and S. M. Atarodi “ Structured Design of Integrated Subscriber Line Interface System and Circuit”, Analog Integrated Circuit and Signal Processing, 45, 47-59, 2005
- [48] Pooya Torkzadeh, Armin Tajalli and S. M. Atarodi “Analysis of Jitter Peaking and Jitter Accumulation in Re-Circulating Delay-Locked Loops” , (ISCAS 2005)
- [49] A. Tajalli, S. M. Atarodi and H. Bazargan “Duty-cycle Controller for Low-Jitter Frequency Doubling DLL” , IEEE Processing-Circuits System , Vol. 152, No.5, October 2005
- [50] A. Tajalli, Paul Muller, S. M. Atarodi and Yusuf Leblebici “A Multichannel 3.5m W/Gbps/Channel Gated Oscillator Based CDR in a 0.18 μ m Digital CMOS Technology” , European Solid State Circuit Conference, (IEEE 2002)
- [51] Pooya Torkzadeh, Armin Tajalli , and Mojtaba Atarodi “ A Wide Tuning Range, 1 GHz-2.5GHz DLL-Based Fractional Frequency Synthesizer / “ IEEE (ISCAS 2005) to be presented

on May 2005

[52] Armin Tajalli, Pooya Torkzadeh, and S. M. Atarodi “ A Fractional Multiplying Delay Locked Loop For On Chip Clock Generation Applications / “ IEEE (ISCAS 2002)

[53] A. Tajalli, Paul Muller, S. M. Atarodi and Yusuf Leblebici “A Low Power, Multichannel Gated Oscillator-Based CDR for Short-Haul Applications” (ISLPED 2005)

[54] J. Grisolia, M. Shalchian, G. Ben. Assayag, H. Coffin, C. Bonafos, S. Schamm, S. M. Atarodi and A Claverie “ The Effect of Oxidation Conditions on Structural and Electrical Properties of Silicon Nanoparticles Obtained by Ultra-Low-Energy Ion Implantation” , Nanotechnology 16(2005) 2987-2992

[55] G. Ben Assayag , M. Shalchian, H. Coffin and A. Claverie, J. Grisolia and C. Dumas, S. M. Atarodi “Electrical Properties of Nanocontacts on Silicon Nanoparticles Embedded in Thin SiO₂ Synthesized by Ultralow Energy Ion Implantation” , J. Vac. Sci. Technology. B22(2) Nov/Dec 2005

[56] S. Mehrmanesh, A. Tajalli and S.M. Atarodi, “A Duty Cycle Control Circuit For High Speed Applications,” IEEE International Symposium on Circuits and Systems, ISCAS2004.

[57] A. Tajalli, Abbas Khodaverdi, Farzad Sahandi and S.M. Atarodi, “Design and Optimization of a High PSRR CMOS Bandgap Voltage Reference,” IEEE International Symposium on Circuits and Systems, ISCAS2004.

[58] M. Shalchian, J. Grisolia, G. Ben Assayag and H. Coffin, S. M. Atarodi “ Room temperature quantum effect in silicon nanoparticles obtained by low-energy implantation and embedded in a nanometer scale capacitor / Applied Physics Letters, 86.163111, 2005

[59] M. Shalchian, J. Grisolia, G. Ben Assayag, H. Coffin, A Claverie and S. M. Atarodi “From Continuous to Quantized Charging Response of Silicon Nanocrystals Obtained by Ultra-Low Energy Ion Implantation.” 2005

[60] A. Tajalli, and S. M. Atarodi “ A Duty-Cycle Controller for Low-Jitter Frequency Doubling DLL, “ To be Published in IEEE Circuits, Devices & Systems Journal (August 2005)

[61] A. Tajalli, and S. M. Atarodi “ A Wide Tuning Range, Fractional Multiplying Delay Locked Loop Topology for Frequency Hopping Applications “ Accepted For Publication on Analog Integrated Circuits & Signal processing Journal. To be published in 2005.

[62] A. Tajalli, and S. M. Atarodi “ Low-Voltage CMOS Transconductor-C-Filter Design

Using Charge-Pump Circuit “Accepted For Publication on Analog Integrated Circuit & Signal processing Journal. To be published in 2005.

[63] A. Tajalli, Paul Muller, S. M. Atarodi and Yusuf Leblebici “A Low Power, Multichannel Gated Oscillator-Based CDR for Short-Haul Applications” (ISLPED 2005)

[64] A. Tajalli, and S. M. Atarodi “ Structured Design of Integrated Subscriber Line Interface System and Circuit “ Accepted For Publication on Analog Integrated Circuit & Signal processing Journal. To be published in 2005.

[65] Pooya Torkzadeh, Armin Tajalli and S. M. Atarodi “Analysis of Jitter Peaking and Jitter Accumulation in Re-Circulating Delay-Locked Loops” , (ISCAS 2005)

[66] A. Tajalli, Paul Muller, S. M. Atarodi and Yusuf Leblebici “A Multichannel 3.5m W/Gbps/Channel Gated Oscillator Based CDR in a 0.18 μ m Digital CMOS Technology” , European Solid State Circuit Conference, (IEEE 2005)

[67] J. Grisolia, M. Shalchian, G. Ben. Assayag, H. Coffin, C. Bonafos, S. Schamm, S. M. Atarodi and A Claverie “ The Effect of Oxidation Conditions on Structural and Electrical Properties of Silicon Nanoparticles Obtained by Ultra-Low-Energy Ion Implantation” ,Institute of Physics Publishin, Nanotechnology 16(2005) 2987-2992

[68] G. Ben Assayag , M. Shalchian, H. Coffin and A. Claverie, J. Grisolia and C. Dumas, S. M. Atarodi “Electrical Properties of Nanocontacts on Silicon Nanoparticles Embedded in Thin Sio₂ Synthesized by Ultralow Energy Ion Implantaion” , J. Vac. Sci. Technology. B23(6) Nov/Dec 2005

[69] M. Shalchian, J. Grisolia, G. Ben Assayag and H. Coffin, S. M. Atarodi “ Roomtemperature quantum effect in silicon nanoparticles obtained by low-energy implantation and embedded in a nanometer scale capacitor / Applied Physics Letters, 86.163111,2005

[70] S. Saeedi, S. Mehrmanesh, A. Tajalli and S.M. Atarodi “A Technique to Suppress Tail Current Flicker Noise in CMOS LC VCOs” IEEE International Symposium on Circuits and Systems,ISCAS2002, pp 3229-3232

[71] A. Tajalli, P. Muller, S.M. Atarodi and Yusuf Leblebici, “Analysis and Modeling of Jitter and Frequency Tolerance in Gated Oscillator Based CDRs” IEEE International Symposium on Circuits and Systems,ISCAS2006, pp 2109-2112

- [72] M. Saffari, S.M. Atarodi , and A. Tajalli, "A $\frac{1}{4}$ Rate Linear Phase Detector for PLL-Based CDR Circuits" IEEE International Symposium on Circuits and Systems, ISCAS2006, pp 3281-3283
- [73] S. Mehrmanesh, Behzad Eghbalkhah, S. Saeedi, Ali Afzali-kusha and S.M. Atarodi "A Compact Low Power Mixed-Signal Equalizer for Gigabit Ethernet Applications" IEEE International Symposium on Circuits and Systems, ISCAS2006, pp 5167-5170
- [74] J.Javidan, M.Atarodi "A different size Transformers and Its Analysis for Power Combiner Blocks in CMOS Technology" IEEE International Conference on Microelectronic 2008
- [75] A.Safarian, F.Sahandi, P.Heydari, M.Atarodi " A Low-Power, Second-Order Δ/Σ Modulator Using A Single Class-AB Op-Amp for Voice-Band Applications" Journal of Analog Circuits and Signal Processing ,2008
- [76] S.Saeedi, M.Atarodi and M.Sharif Bakhtiar " A Divide-by-2 Frequency Divider for I/Q Generation in a Multi-band Frequency Synthesizer " IEEE Asia Pacific Conference on Circuits and Systems, pp 1383-1386
- [77] J.Javidan, M.Atarodi "A 1W, 900MHz Class-F RF Power Amplifier with 56% PAE in 0.18-um CMOS Technology " IEEE International Conference on Microelectronic 2008
- [78] J.Javidan, M.Atarodi, Howard C.Luong "A Fully Integrated 900MHz Power Amplifier with new Transformer Network in CMOS Technology" Journal of International Review of Electrical Engineering
- [79] P.Torkzadeh, M.Atarodi, J.Javidan "A 640MHz CMOS Switched-Capacitor $\Sigma\Delta$ ADC with 12 Bits of Resolution by Using Double Sampling Technique" IEEE ICCSC 2008
- [80] P.Torkzadeh, M.Atarodi, J.Javidan "An Elaborate Design and Implementation of a 3rd order Sigma-Delta Modulator ADC for Wideband Signals" IEEE ICCSC 2008
- [81] P.Torkzadeh, M.Atarodi "Behavioral modeling of clock feed-through and channel charge injection non-ideal effects in SIMULINK for switched-capacitor integrator" Submitted to the journal of International Review of Electrical Engineering.
- [82] M. Chahardori, M.Atarodi, S.F Ashrafi " A New Low-Voltage, High PSRR, CMOS Bandgap Voltage Reference" Published in Proceedings System-On-Chip Conference 2008.

[83] P.Torkzadeh, M.Atarodi “A Low Power, Low Phase Noise, Square Wave LC Quadrature VCO for ISM Band” accepted for presentation in IEEE NEWCAS conference 2009

[84] P.Torkzadeh, M.Atarodi “A 240MHz, 12mW CMOS Switched-Capacitor Sigma-Delta Modulator ADC with 14 Bits of Resolution Using Double Sampling Technique” Journal of International Review of Electrical Engineering.

[85] J.Javidan, M.Atarodi “A different size Transformers and Its Analysis for Power Combiner Blocks in CMOS Technology” accepted for presentation in IEEE NEWCAS conference 2009.